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TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.
SEC.506

In Re Application Of: Byoung-Taek LEE et al.

Serial No.
09/276,803

Filing Date
26 March 1999

Examiner
Berezny, Neal

Group Art Unit
2823

Title: **METHOD FOR MANUFACTURING CAPACITOR OF SEMICONDUCTOR DEVICE HAVING
DIELECTRIC LAYER OF HIGH DIELECTRIC CONSTANT**

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SUPPLEMENTAL APPEAL BRIEF

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Dated: 1 March 2004

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Serial Number 09/276,803
SEC.506
Appeal Brief Dated 1 March 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of:

Byoung-taek LEE et al.

Group Art Unit: 2823

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Examiner: Neal BEREZNY

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METHOD FOR MANUFACTURING
CAPACITOR OF SEMICONDUCTOR
DEVICE HAVING DIELECTRIC
LAYER OF HIGH DIELECTRIC
CONSTANT

SUPPLEMENTAL APPEAL BRIEF

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Sir:

Supplemental to the Appeal Brief filed on 27 February 2004 in response to the FINAL rejection of all of the pending claims 1, 2, 4, 5, 7-16, and 18-20 in the Office Action dated 29 September 2003, and in support of the "Notice of Appeal" filed on 29 December 2003, Applicants hereby submit this Supplemental Appeal Brief.

By this Supplemental Appeal Brief, Applicants correct two typographical errors in the Appeal Brief one each on pages 9 and 13.

In particular, on page 9 of the Appeal Brief, the last full paragraph beginning at line 18, should read:

“Furthermore, as noted above, the first and second post-annealings of claim 9 are performed in completely different environments. The first post-annealing of claim 9 is performed in an inert environment, while the second post-annealing of claim 9 is performed in an oxygen environment.”

Also, on page 12 of the Appeal Brief, the paragraph beginning on line 5 should read:

“Furthermore, as noted above, the first and second post-annealings of claim 1 are performed in completely different environments. The first post-annealing of claim 1 is performed in an inert environment, while the second post-annealing of claim 1 is performed in an oxygen environment.”

Applicants have also attached to this Supplemental Appeal Brief replacement sheets for pages 9 and 12 of the Appeal Brief. Applicants regret any confusion engendered by these errors.

Accordingly, entry of this Supplemental Appeal Brief is respectfully requested.

Respectfully submitted,

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Date: 1 March 2004

By: _____



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electrode” - second anneal (1) (step 94) and second anneal (2) (step 96).¹ Watanabe does not disclose that the second anneal (2) (step 96) is performed “at a second temperature lower than the first temperature” at which the second anneal (1) (step 94) is performed. Indeed, col. 15, lines 12-14 clearly state that “[t]he second anneal is preferably performed in an electric furnace at a temperature” Thus, Watanabe teaches that the second anneal (which includes both steps 94 and 96) is performed at a (singular) temperature. This is in direct contrast to the method of claim 9 where the second post-annealing (e.g., step 96 of Watanabe) is performed at a temperature that is lower than the temperature at which the first post-annealing (e.g., step 94 of Watanabe) is performed.

Therefore, the second anneal (1) (step 94) and second anneal (2) (step 96) of Watanabe cannot correspond to the first and second post-annealings of claim 9.

So, for at least this reason, Applicants respectfully submit that claim 9 is clearly patentable over Watanabe under 35 U.S.C. § 102.

Furthermore, as noted above, the first and second post-annealings of claim 9 are performed in completely different environments. The first post-annealing of claim 9 is performed in an inert environment, while the second post-annealing of claim 9 is performed in an oxygen environment.

Watanabe does not disclose that the second anneal (2) (step 96) is performed in

¹ Watanabe clearly discloses that the annealing step 92 is performed before forming the plate electrode, and therefore it cannot correspond to any of the post-annealing steps of claim 9 (see FIG. 1).

performed *in-situ*, does not remedy this shortcoming of Watanabe.

So, for at least this reason, Applicants respectfully submit that claim 1 is clearly patentable over any possible combination of Watanabe and Ping under 35 U.S.C. § 103.

Furthermore, as noted above, the first and second post-annealings of claim 1 are performed in completely different environments. The first post-annealing of claim 1 is performed in an inert environment, while the second post-annealing of claim 1 is performed in an oxygen environment.

Watanabe does not disclose that the second anneal (2) (step 96) is performed in a different environment than that at which the second anneal (1) (step 94) is performed. Indeed, Watanabe teaches that the second anneal (which comprises both steps 94 and 96) should be performed in an oxygen environment (see col. 15, lines 35-39).

So, for at least this additional reason, Applicants respectfully submit that claim 1 is clearly patentable over any possible combination of Watanabe and Ping under 35 U.S.C. § 103.

Finally, neither Watanabe and Ping disclose performing two annealing processes under two very different environments (inert environment, and oxygen environment) *in situ*. So no combination of Watanabe and Ping could produce the method of claim 1.

Moreover, to the extent that the Examiner is taking “Official Notice” that it